

FDS6680AS

30V N-Channel PowerTrench® SyncFET™

Features

- 11.5 A, 30 V. $R_{DS(ON)}$ max = 8.0 m Ω @ V_{GS} = 10 V $R_{DS(ON)}$ max = 10.5 m Ω @ V_{GS} = 4.5 V
- Includes SyncFET Schottky body diode
- Low gate charge (22nC typical)
- High performance trench technology for extremely low R_{DS(ON)} and fast switching
- High power and current handling capability

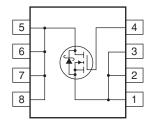
Applications

- DC/DC converter
- Low side notebooks

General Description

The FDS6680AS is designed to replace a single SO-8 MOSFET and Schottky diode in synchronous DC:DC power supplies. This 30V MOSFET is designed to maximize power conversion efficiency, providing a low $R_{\rm DS(ON)}$ and low gate charge. The FDS6680AS includes an integrated Schottky diode using Fairchild's monolithic SyncFET technology. The performance of the FDS6680AS as the low-side switch in a synchronous rectifier is indistinguishable from the performance of the FDS6680 in parallel with a Schottky diode.





Absolute Maximum Ratings T_A = 25°C unless otherwise noted

Symbol	Parameter		Ratings	Units	
V _{DSS}	Drain-Source Voltage		30	V	
V _{GSS}	Gate-Source Voltage		±20	V	
I _D	Drain Current - Continuous	(Note 1a)	11.5	A	
	- Pulsed		50		
P _D	Power Dissipation for Single Operation	(Note 1a)	2.5	W	
		(Note 1b)	1.2		
		(Note 1c)	1		
T _J , T _{STG}	Operating and Storage Junction Temperature Range		−55 to +150	°C	
Thermal Characteristics					
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	(Note 1a)	50	°C/W	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	(Note 1)	25	°C/W	

Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape width	Quantity
FDS6680AS	FDS6680AS	13"	12mm	2500 units
FDS6680AS	FDS6680AS_NL (Note 4)	13"	12mm	2500 units

Electrical Characteristics $T_A = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Charac	teristics			•		
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 1 \text{ mA}$	30			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	I _D = 1 mA, Referenced to 25°C		29		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 24 V, V _{GS} = 0 V			500	μА
I _{GSS}	Gate-Body Leakage	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$			±100	nA
On Charac	teristics (Note 2)		•			
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 1 \text{ mA}$	1	1.5	3	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	I _D = 1 mA, Referenced to 25°C		-3		mV/°C
R _{DS(on)}	Static Drain–Source On–Resistance	$V_{GS} = 10 \text{ V}, I_D = 11.5 \text{ A}$ $V_{GS} = 4.5 \text{ V}, I_D = 9.5 \text{ A}$ $V_{GS} = 10 \text{ V}, I_D = 11.5 \text{ A}, T_J = 125^{\circ}\text{C}$		6.5 8.3 12.3	8 10.5 15.5	mΩ
I _{D(on)}	On-State Drain Current	V _{GS} = 10 V, V _{DS} = 5 V	50			Α
9 _{FS}	Forward Transconductance	V _{DS} = 15 V, I _D = 11.5 A		48		S
Dynamic C	haracteristics		•	•	1	
C _{iss}	Input Capacitance	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V},$		1240		pF
C _{oss}	Output Capacitance	f = 1.0 MHz		350		pF
C _{rss}	Reverse Transfer Capacitance			120		pF
R _G	Gate Resistance	V _{GS} = 15 mV, f = 1.0 MHz		1.4		Ω
Switching	Characteristics (Note 2)		•	•	1	
t _{d(on)}	Turn-On Delay Time	V _{DS} = 15 V, I _D = 1 A,		9	18	ns
t _r	Turn-On Rise Time	V_{GS} = 10 V, R_{GEN} = 6 Ω		5	10	ns
t _{d(off)}	Turn-Off Delay Time			27	42	ns
t _f	Turn-Off Fall Time			11	21	ns
t _{d(on)}	Turn-On Delay Time	V _{DS} = 15 V, I _D = 1 A,		11	20	ns
t _r	Turn-On Rise Time	$V_{GS} = 4.5 \text{ V}, R_{GEN} = 6 \Omega$		12	22	ns
t _{d(off)}	Turn-Off Delay Time			18	32	ns
t _f	Turn-Off Fall Time			11	20	ns
Q _{g(TOT)}	Total Gate Charge at Vgs = 10V	V _{DD} = 15 V, I _D = 11.5 A,		22	30	nC
Qg	Total Gate Charge at Vgs = 5V			12	16	nC
Q _{gs}	Gate-Source Charge			3.5		nC
Q _{gd}	Gate-Drain Charge			3.4		nC

Electrical Characteristics $T_A = 25^{\circ}C$ unless otherwise noted (Continued)

Symbol	Parameter	Test Conditio	ns	Min	Тур	Max	Units
Drain-Sour	Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain-Source Diode Forward Current					3.5	Α
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 3.5 A V _{GS} = 0 V, I _S = 7 A	(Note 2) (Note 2)		0.5 0.6	0.7	V
T _{rr}	Diode Reverse Recovery Time	I _F = 11.5A,			18		nS
Q _{rr}	Diode Reverse Recovery Charge	$d_{iF}/d_t = 300 \text{ A/}\mu\text{s}$	(Note 3)		12		nC

Notes:

1. R_{BJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{BJC} is guaranteed by design while R_{BCA} is determined by the user's board design.



a) 50°/W when mounted on a 1 in² pad of 2 oz copper



b) 105°/W when mounted on a .04 in² pad of 2 oz copper



c) 125°/W when mounted on a minimum pad.



Scale 1:1 on letter size paper

- 2. Pulse Test: Pulse Width $< 300 \mu s$, Duty Cycle < 2.0%
- 3. See "SyncFET Schottky body diode characteristics" below.
- 4. FDS6680AS_NL is a lead free product. The FDS6680AS_NL marking will appear on the reel label.

Typical Characteristics

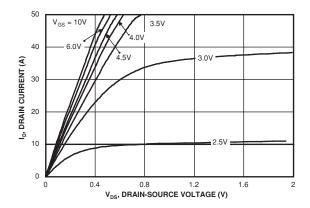


Figure 1. On-Region Characteristics.

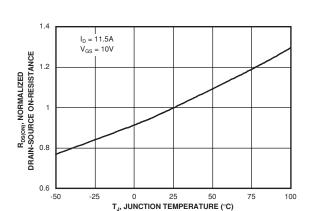


Figure 3. On-Resistance Variation with Temperature.

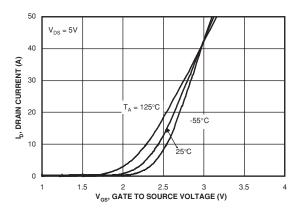


Figure 5. Transfer Characteristics.

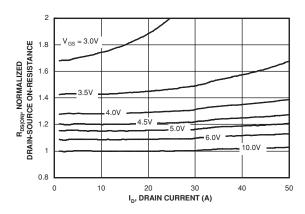


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

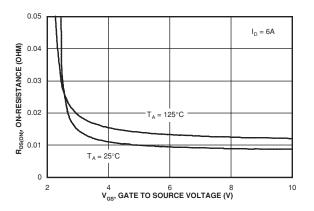


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

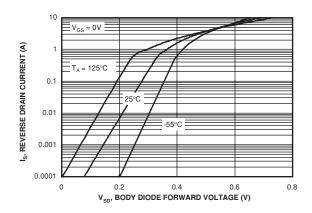
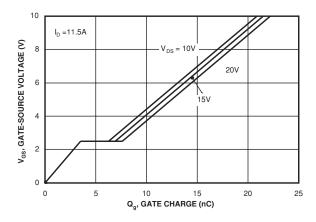


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

Typical Characteristics (continued)



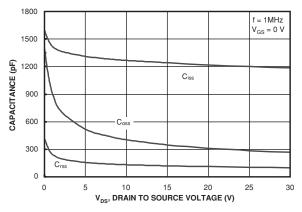
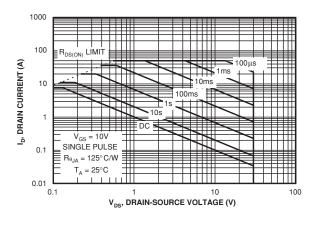


Figure 7. Gate Charge Characteristics.





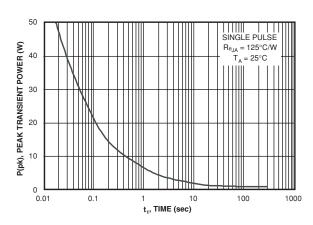


Figure 9. Maximum Safe Operating Area.

Figure 10. Single Pulse Maximum Power Dissipation.

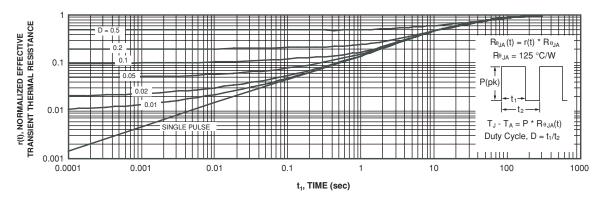


Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 1c. Transient thermal response will change depending on the circuit board design.

5

Typical Characteristics (continued)

SyncFET Schottky Body Diode Characteristics

Fairchild's SyncFET process embeds a Schottky diode in parallel with PowerTrench MOSFET. This diode exhibits similar characteristics to a discrete external Schottky diode in parallel with a MOSFET. Figure 12 shows the reverse recovery characteristic of the FDS6680AS.

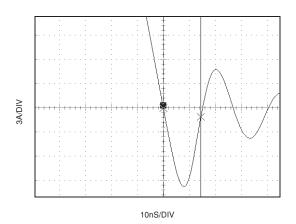


Figure 12. FDS6680AS SyncFET body diode reverse recovery characteristic.

For comparison purposes, Figure 13 shows the reverse recovery characteristics of the body diode of an equivalent size MOSFET produced without SyncFET (FDS6680).

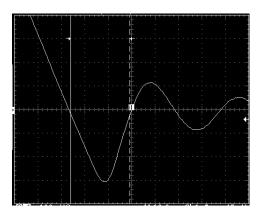


Figure 13. Non-SyncFET (FDS6680) body diode reverse recovery characteristic.

Schottky barrier diodes exhibit significant leakage at high temperature and high reverse voltage. This will increase the power in the device.

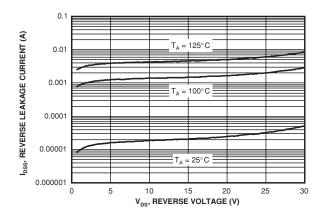


Figure 14. SyncFET body diode reverse leakage versus drain-source voltage and temperature.

Typical Characteristics

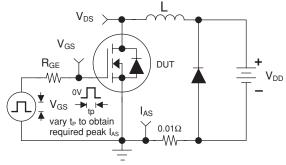


Figure 15. Unclamped Inductive Load Test Circuit

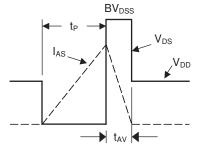


Figure 16. Unclamped Inductive Waveforms

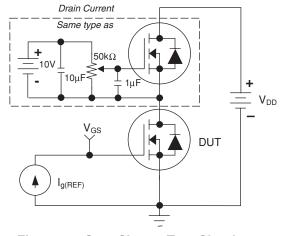


Figure 17. Gate Charge Test Circuit

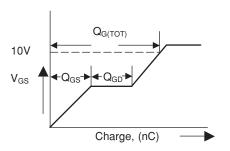


Figure 18. Gate Charge Waveform

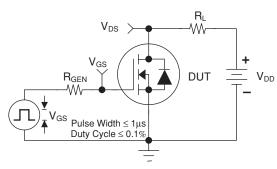


Figure 19. Switching Time Test Circuit

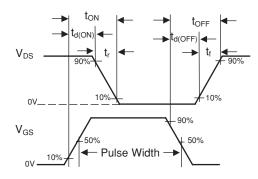


Figure 19. Switching Time Waveforms

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

$ACEx^{TM}$	FAST®	IntelliMAX™	РОР™	SPM™
ActiveArray™	FASTr™	ISOPLANAR™	Power247™	Stealth™
Bottomless™	FPS™	LittleFET™	PowerEdge™	SuperFET™
CoolFET™	FRFET™	MICROCOUPLER™	PowerSaver™	SuperSOT™-3
CROSSVOLT™	GlobalOptoisolator™	MicroFET™	PowerTrench®	SuperSOT™-6
DOME™	GTO™ .	MicroPak™	QFET®	SuperSOT™-8
EcoSPARK™	HiSeC™	MICROWIRE™	QS TM	SyncFET™
E ² CMOS TM	I ² C TM	MSX™	QT Optoelectronics™	TinyLogic [®]
EnSigna™	<i>i-Lo</i> ™	MSXPro™	Quiet Series™	TINYOPTO™
FACT™	ImpliedDisconnect™	OCX^{TM}	RapidConfigure™	TruTranslation™
FACT Quiet Series™		OCXPro [™]	RapidConnect™	UHC™
Across the board. Around the world.™ The Power Franchise® Programmable Active Droop™		OPTOLOGIC [®] OPTOPLANAR™ PACMAN™	µSerDes™ SILENT SWITCHER® SMART START™	UltraFET® UniFET™ VCX™

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
- A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

Rev. I15